

APPLICATION

OF

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FOR

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ON

HIGH-DENSITY PCB TEST JACK

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## HIGH-DENSITY PCB TEST JACK

BACKGROUND OF THE INVENTIONField of the Invention

This invention relates to the field of printed circuit board (PCB) test jacks, and particularly to test jacks for use on high-density PCBs.

Description of the Related Art

Printed circuit boards (PCBs) are often tested using a test probe which contacts a signal trace of interest. In some instances, test jacks are mounted onto the PCB to facilitate such testing. To provide signal and ground connections to the test probe, the test jack typically employs two or more through-hole pins that are inserted into through-holes on the PCB. These through-holes are bored all the way through the PCB, where they pass through signal and ground traces as necessary to effect a desired measurement. The through-hole pins of the test jack are soldered into their respective holes, thereby securing the jack to the PCB and providing the electrical connections.

However, when a PCB is a multi-layer board carrying a high-density of signal traces, each of the two or more through-holes required by the test jack can interfere with the routing of signals on the top and inner layers of the

board. In addition, the many signal traces can force the through-holes to be located some distance away from their respective sources, which can adversely affect the quality of the probed signal.

#### SUMMARY OF THE INVENTION

The present test jack comprises a body portion arranged to interface with an external test probe, at least one surface mount conductor connected to the body portion and arrayed for attaching to corresponding surface pads on the surface of a PCB, and a signal conductor connected to the body portion. The signal conductor comprises a single through-hole pin for insertion into a corresponding single through-hole on the PCB. Using only a single through-hole pin reduces signal routing interference in the PCB when compared with a test jack employing multiple through-hole pins.

Alternatively, a test jack in accordance with the present invention may employ no through-hole pins. In this case, the signal conductor is also a surface mount-type, which is connected to the body portion and arrayed for attaching to a corresponding surface pad on the surface of a PCB.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Further features and advantages of the invention will be apparent to those skilled in the art from the following

detailed description, taken together with the accompanying drawings:

FIG. 1a is a plan view of a test jack per the present invention.

FIG. 1b is a side elevation view of the test jack shown in FIG. 1a.

FIGS. 2a-2c are plan views of alternate embodiments of the present test jack.

FIG. 3a is a plan view of another possible embodiment of a test jack per the present invention.

FIG. 3b is a side elevation view of the test jack shown in FIG. 3a.

FIG. 4a is a plan view of another possible embodiment of a test jack per the present invention.

FIG. 4b is a side elevation view of the test jack shown in FIG. 4a.

#### DETAILED DESCRIPTION OF THE INVENTION

A PCB test jack in accordance with the present invention is shown in FIGS. 1a (plan view) and 1b (side elevation view). The test jack 10 comprises a body portion 12, preferably cylindrical, which provides a mechanical and electrical interface to a test probe (not shown). The test jack body typically provides two connections for a test probe: a ground or shield connection 14, and a "signal" connection 16 which conveys a signal of interest. A test probe suitable for use with test jack 10, such as an oscilloscope probe, is designed to make contact with the

jack's ground and signal connections when inserted into the jack body. Note that, although connection 14 is referred to herein as a "ground" connection, it may carry other, non-ground signals instead.

To provide a connection to a signal of interest, test jack 10 is mounted on a printed-circuit board (PCB) 18. To facilitate the mounting, test jack 10 includes a signal conductor that preferably comprises a single through-hole pin 20; through-hole pin 20 fits into a corresponding through-hole 22 bored through PCB 18. An electrical connection 23 is effected between through-hole pin 20 and signal connection 16 within the body 12 of the test jack. Through-hole 22, which is typically a plated-through hole, is positioned such that it passes through a signal trace which carries the signal of interest; the signal trace may be located on the top, bottom, or an inner layer of PCB 18.

Through-hole pin 20 is soldered into through-hole 22, such that an electrical connection is provided between the signal of interest and pin 20 (and thus signal connection 16). Soldering pin 20 into hole 22 also firmly affixes test jack 10 to PCB 18, providing mechanical strength to the mounting. Through-hole pin 20 can be soldered into through-hole 22 using, for example, a wave soldering machine or by hand.

To provide a connection to an appropriate ground or shield (or other, non-ground signal) trace, test jack 10 includes at least one surface mount conductor 24, which extends generally perpendicularly from the jack's body 12

and is affixed to a corresponding surface pad 26 on the surface of PCB 18. The surface pads can be connected to inner PCB layers using vias, which can be more flexibly located on the PCB and thus minimize interference with signals routed on the inner layers. The PCB preferably comprises surface pads that align with the test jack's surface mount conductors 24 when the jack's through-hole pin 20 is installed into through-hole 22. When so installed, surface mount conductors 24 are soldered to their corresponding surface pads. This may be accomplished by, for example, using solder paste to lightly "glue" the conductors to their corresponding pads, and then passing the PCB through a solder reflow machine to melt the solder paste and form a permanent connection between the conductors and the PCB.

Providing a test jack with just a single through-hole pin reduces the degree to which signal routing on and within the PCB is interfered with by the presence of the jack. This is especially important with high-density PCBs, which typically include at least 4 layers and have a large number of closely-spaced signal traces on each layer. Test jacks which each require multiple through-holes can severely impact signal routing within this type of board.

The present PCB test jack requires at least one surface mount conductor 24. The jack in FIGs. 1a and 1b depict the use of two such conductors, equally-spaced 180° apart around the jack body. Other possible arrangements are shown in the plan views of FIGs. 2a, 2b and 2c. FIG.

2a shows the use of a single surface mount conductor 24, FIG. 2b shows the use of three surface mount conductors 24, equally spaced  $120^\circ$  apart, and FIG. 2c shows four conductors 24, spaced  $90^\circ$  apart. The number and arrangement of surface mount conductors 24 is preferably application-specific: if the test jack's mounting requires a high degree of mechanical strength, a greater number of surface mount conductors can be employed. On the other hand, if interference with signal routing on the top surface of the PCB is a primary concern, a lesser number of surface mount conductors can be used.

Another option for surface mount conductors 24 is shown in FIGs. 3a (plan view) and 3b (side elevation view).

Here, surface mount conductor 24 is ring-shaped, encircling and extending perpendicularly from the body 12 of test jack 10. The PCB to which this type of test jack would be mounted would include a corresponding ring-shaped surface pad, to which the surface mount conductor would be soldered.

An alternative embodiment of the ring-shaped surface mount conductor shown in FIGs. 3a and 3b is shown in the plan view of FIG. 3c. Here, the conductor includes thermal reliefs, i.e., small separations in the metal, periodically spaced around the ring. The thermal reliefs serve to keep heat applied to one part of the contact localized in that area, thereby improving the ability to melt solder.

Though the use of a single through-hole pin is preferred, this is not essential to the invention, which

also contemplates a test jack having no through-hole pins.

Such a jack is shown in FIGs. 4a (plan view) and 4b (side elevation view). As before, the jack includes a body portion 30 which includes a signal connection 32 and a ground connection 34 for interfacing to an external test probe. Here, however, rather than connect signal connection 32 to the PCB using a through-hole pin, a surface mount conductor 36 is used. Ground connection 34 is connected to the PCB using one or more surface mount conductors 38, as described above. In this way, both the test jack's signal and ground connections are effected using surface mount conductors, which are soldered to corresponding surface pads on the PCB surface. As a result, disruption of signal routing on the inner board layers due to through-holes is further reduced. Note, however, that if more mechanical strength is needed, to perform long term or repetitive test measurements, for example, a test jack using a single through-hole pin as described above is preferred.